

FIG. 1 (Prior Art)

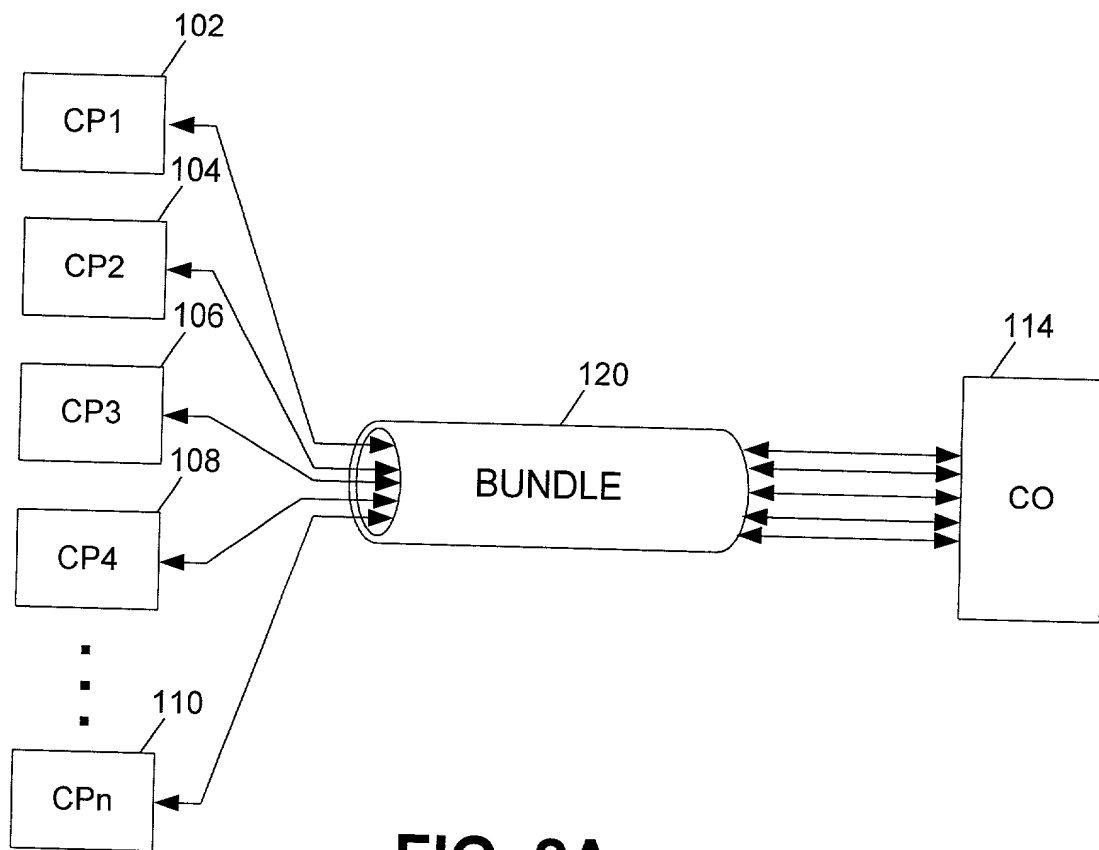


FIG. 2A

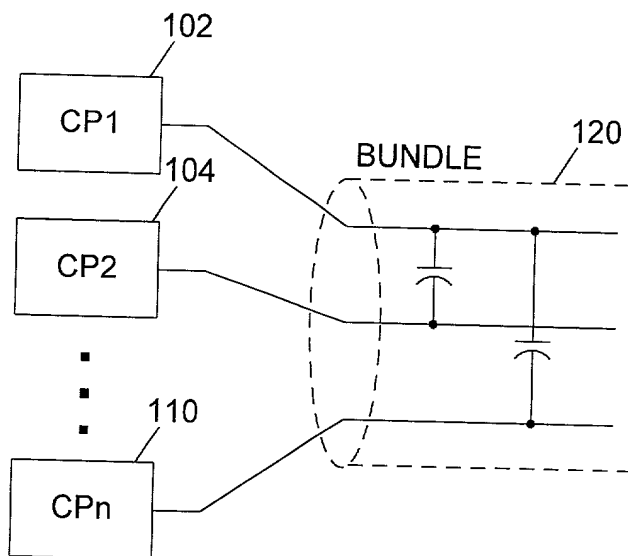


FIG. 2B

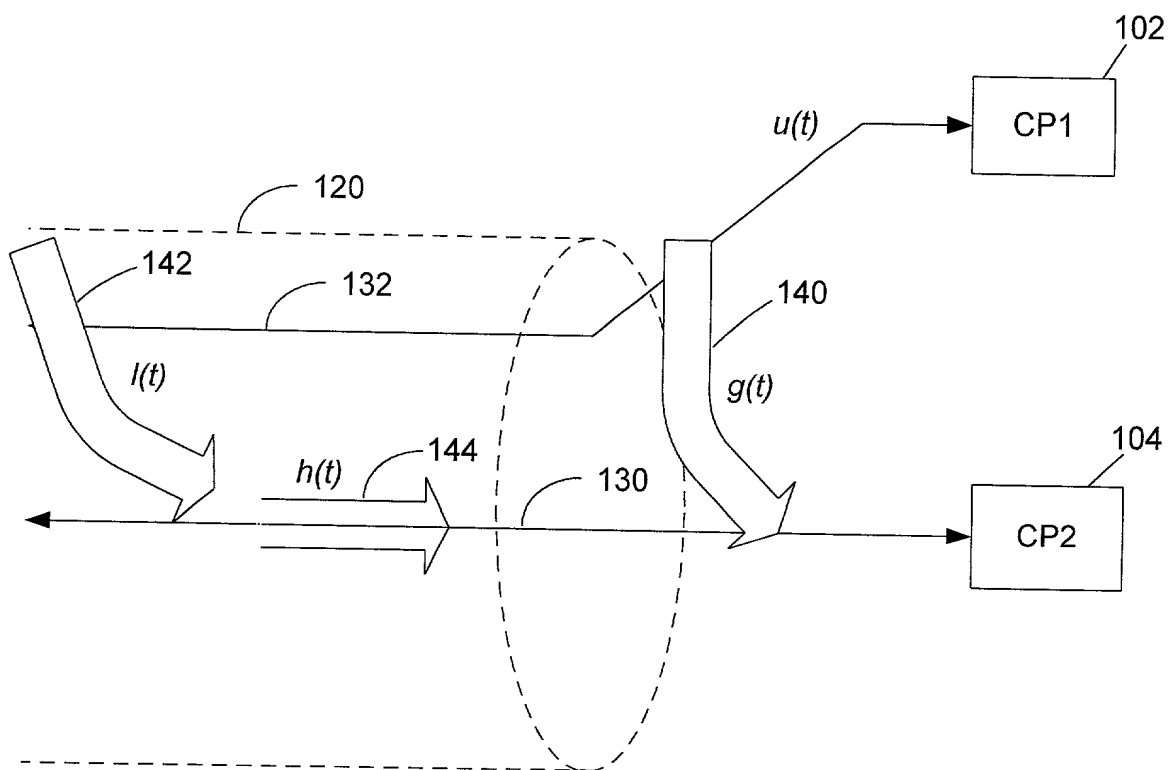


FIG. 3A

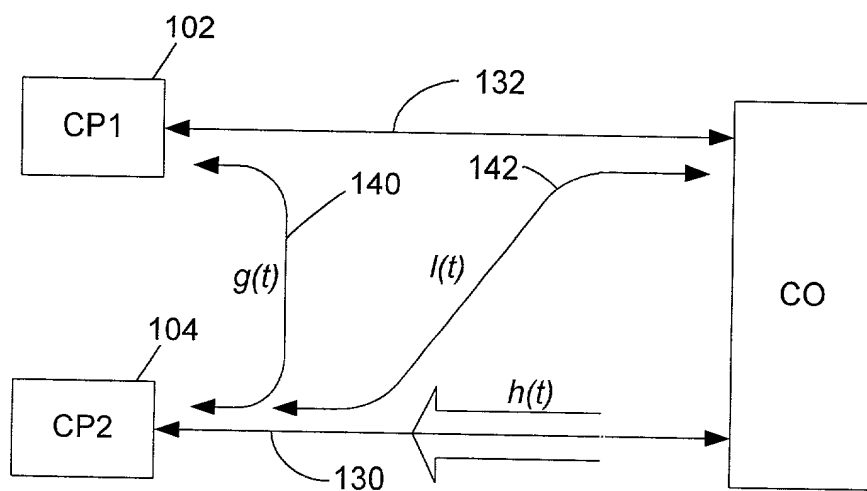


FIG. 3B

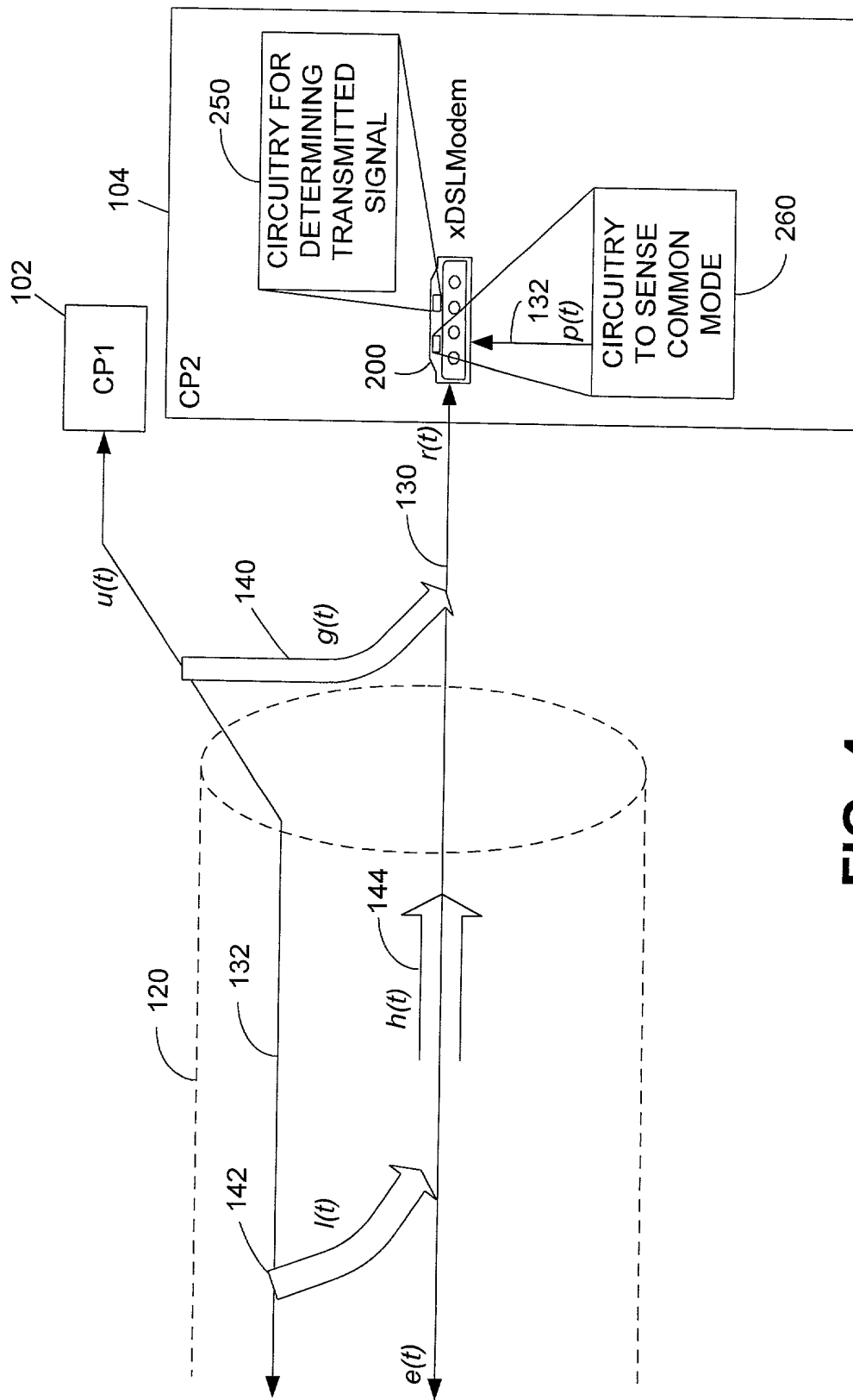


FIG. 4

DSL MODEM 200

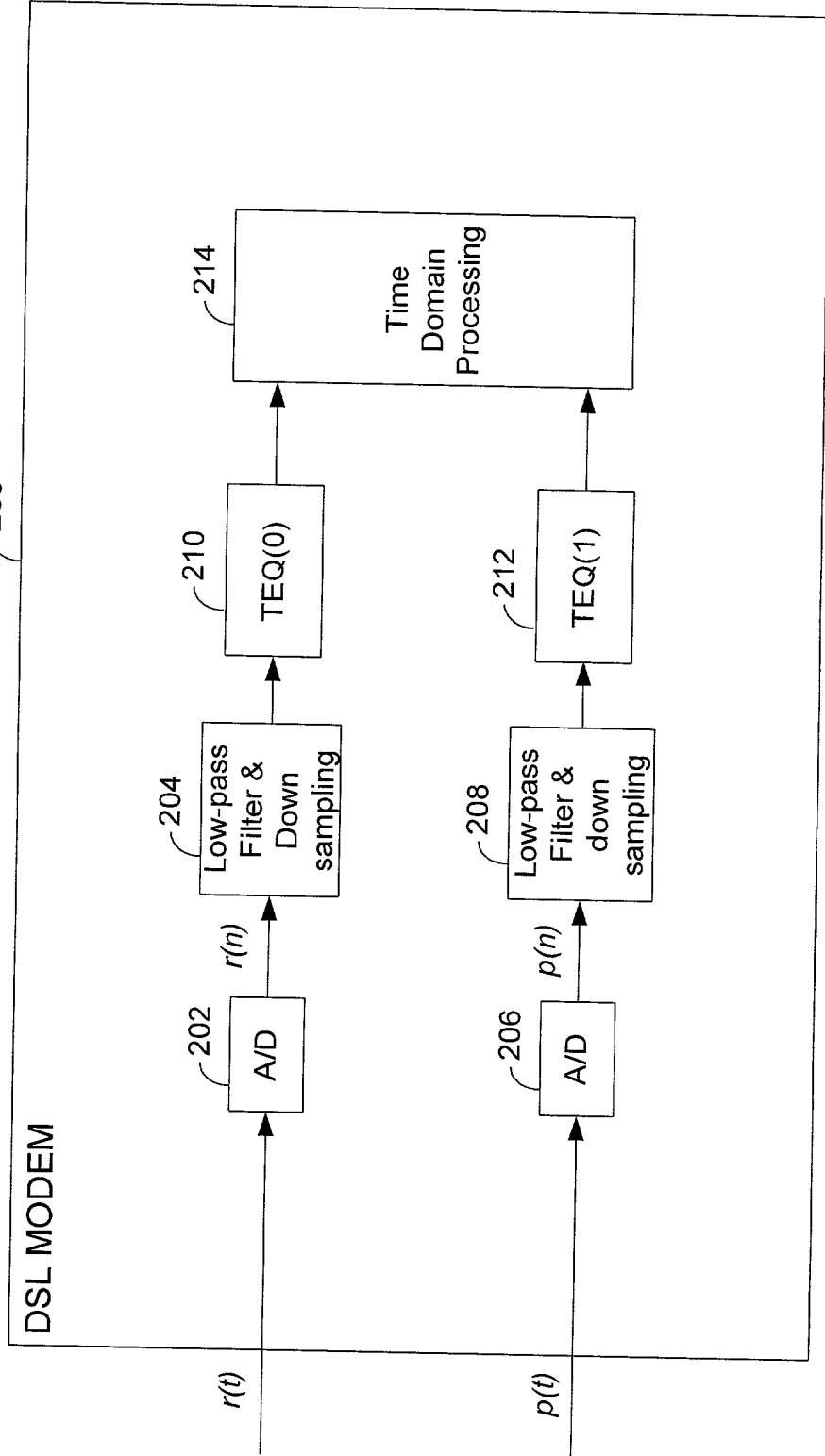


FIG. 5

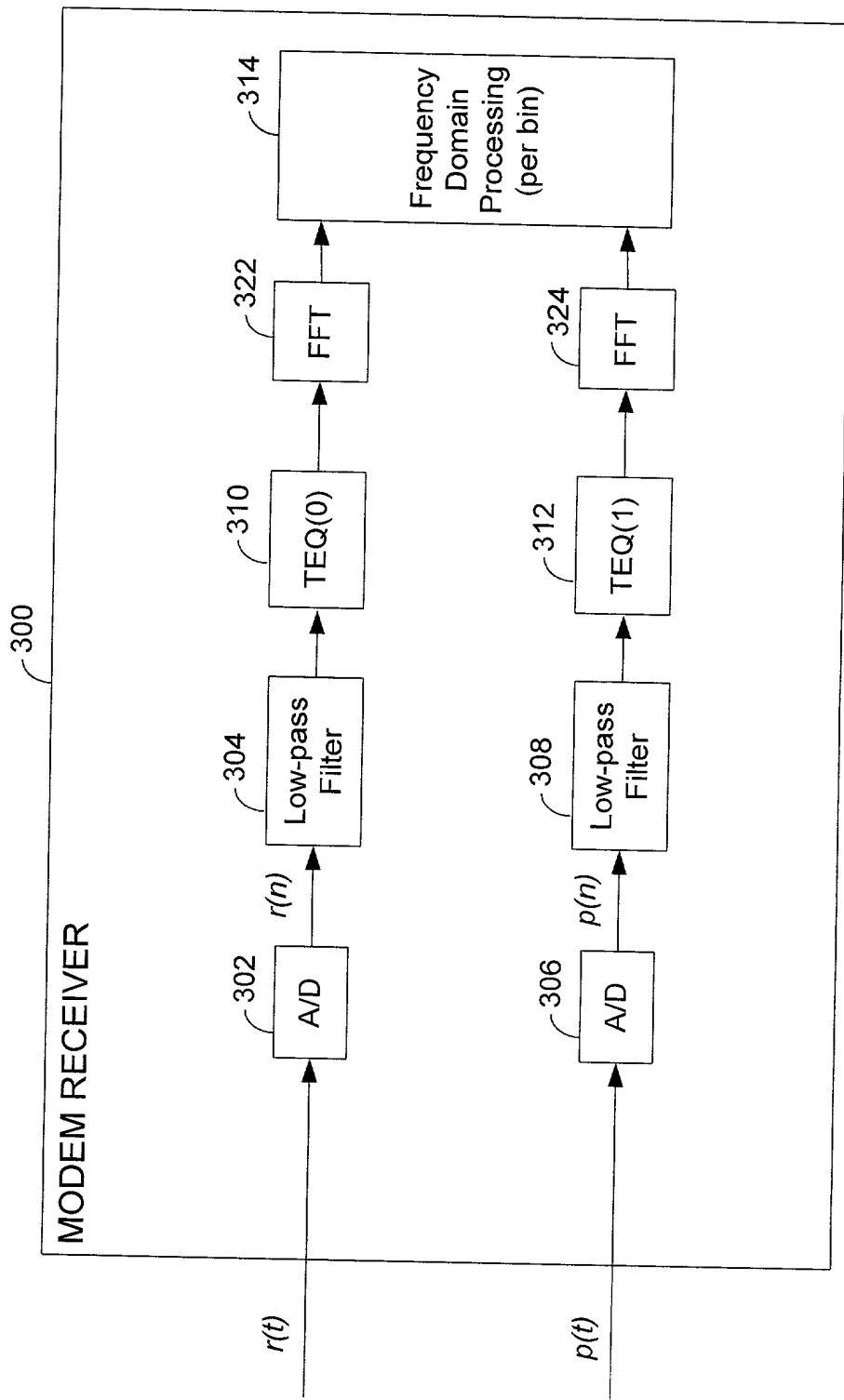


FIG. 6

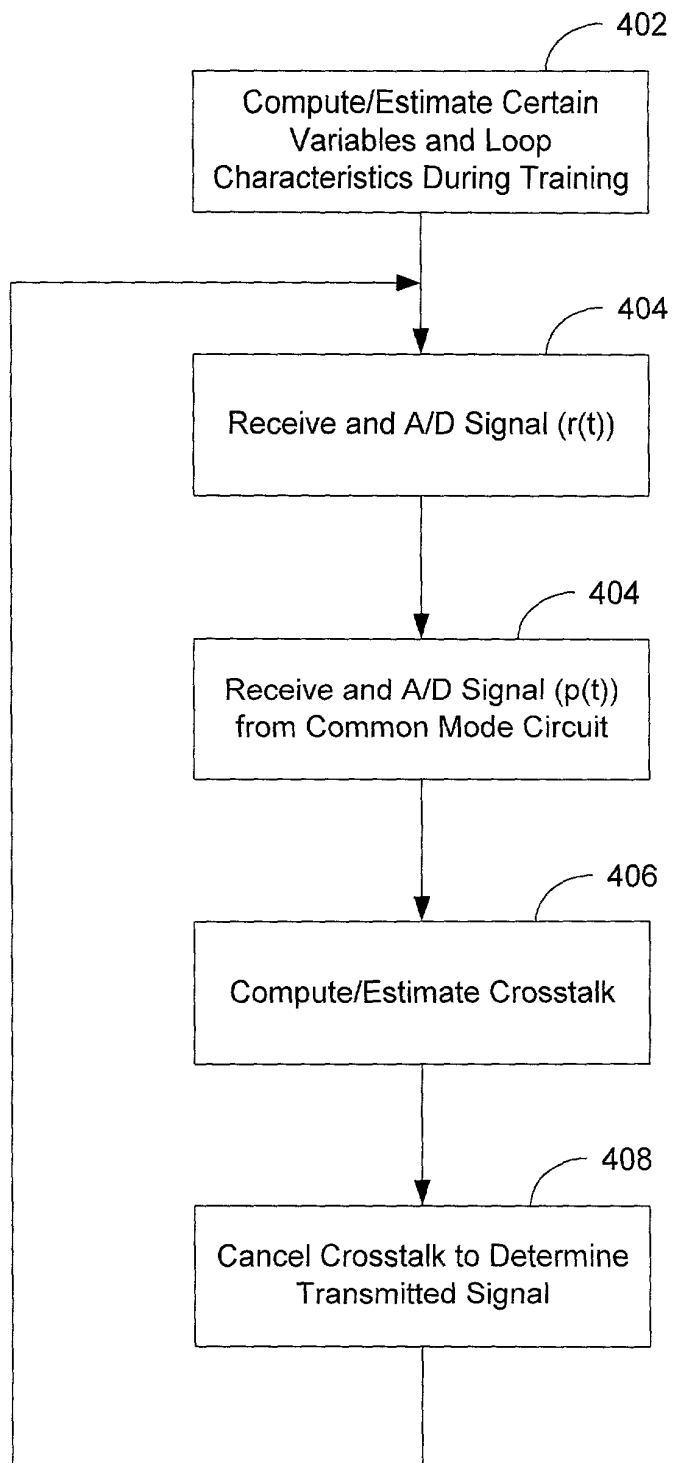


FIG. 7

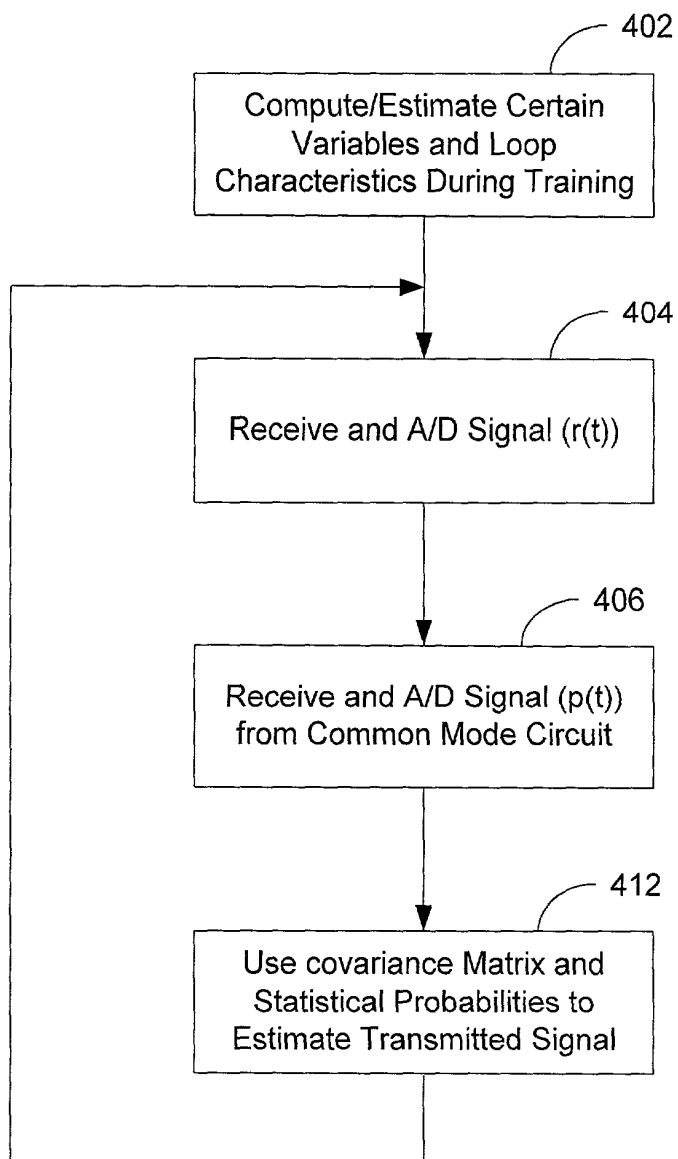


FIG. 8

FIG. 9 is a schematic diagram of a system 200 for processing signals from a Tip and a Ring. The system includes a transformer T1 with a 2:1 turns ratio, capacitors C1 and C2, a resistor network, and a second transformer T2 with a 4:1 turns ratio. The output of T2 is connected to a network of resistors R1, R2 and a capacitor C4, which is then connected to a CP AFE (Control Plane Analog Front End) block containing a Hybrid Amp.

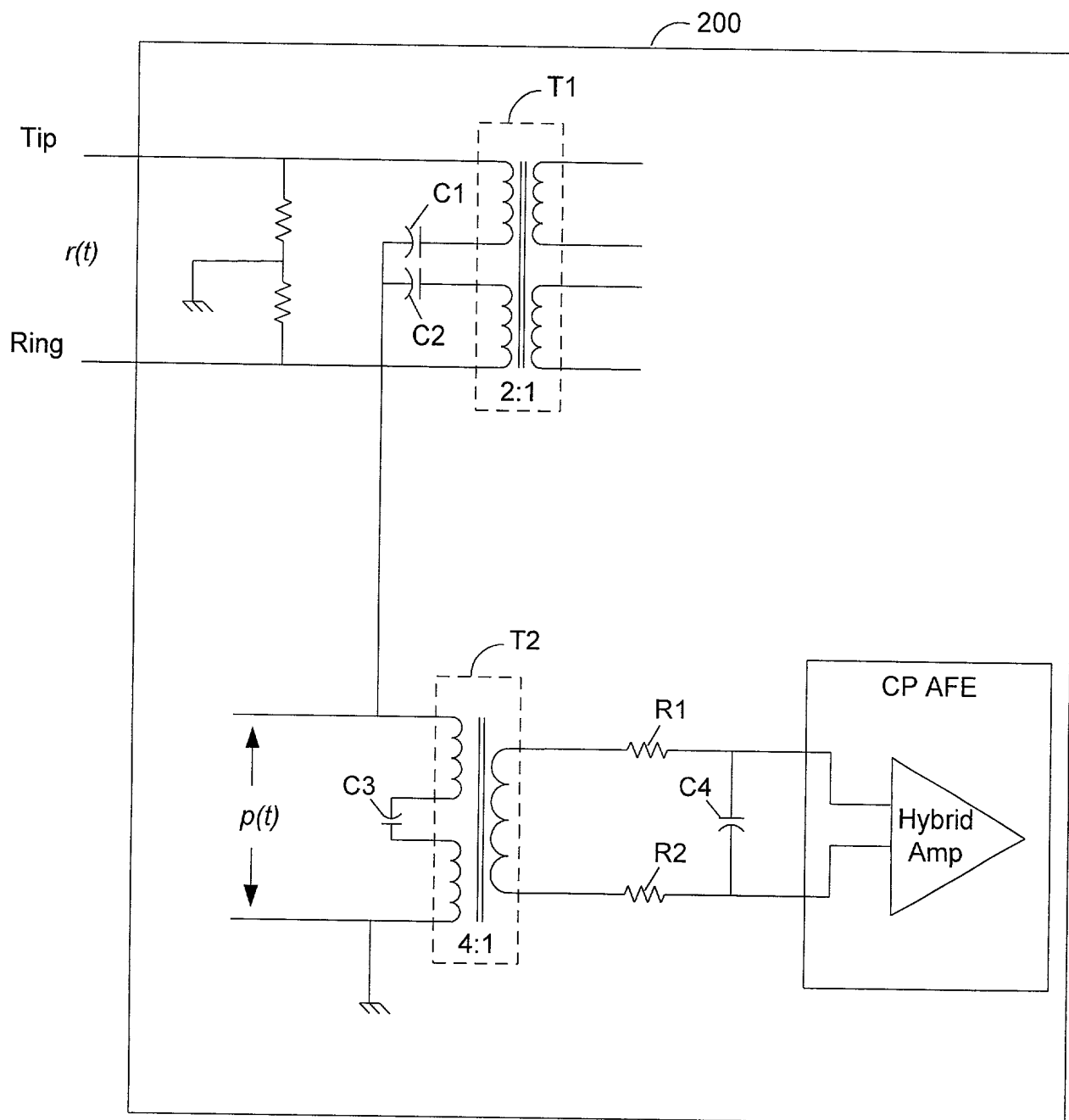


FIG. 9